Notic of Allowability	Application No.	Applicant(s)
	10/647,993	PURI ET AL.
	Examiner	Art Unit
	Pho M Luu	2824
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>Amendment filed on 04/09/04</u> .		
2. The allowed claim(s) is/are 1-8 and 10-25.		
3. The drawings filed on <u>09 April 2004</u> are accepted by the Examiner.		
4.		
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☑ Other <i>Updated Sear</i>	e nent/Comment ent of Reasons for Allowance
	SUPERVI	SORY PATENT EXAMINED

U.S. Patent and Trademark Office PTOL-37 (Rev. 1-04)

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## **Allowance**

1. Acknowledgment is made of applicant's Amendment, filed 09 April 2004.

The changes and remarks disclosed therein were considered.

2. The corrected or substitute drawings were received on 09 April 2004.

These drawing are acceptable.

3 Claim 9 have been canceled.

Claims 13-25 are newly added.

Claims 1-8 and 10-25 are pending in the application.

4 Claims 1-8 and 10-25 are allowance.

The following is an examiner's statement of reasons for allowance:

There is no teaching or suggestion in the prior art to:

"a plurality of fuse blocks connected to the fuse controller, the BISR scheme configured such that the memories share the fuse block said BISR scheme configured to performed a wafer sort" as claimed in the independent claim 1; or

"a plurality of fuse blocks connect to the fuse controller and having the memories share the fuse block further comprising performing a wafer sort" as claimed in the independent claim 8; or

"a plurality of fuse blocks connect to the fuse controller, the BISR scheme configured such that the memories share the fuse blocks, wherein the memories are configured in a serial chain, wherein one of the memories is connected to the fuse controller and the other memories are serially connected to each other and

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to the memory which is connected to the fuse controller" as claimed in the independent claim 13; or

"the memories share the fuse blocks such that the memories are configured in a serial chain, which one of the memories is connected to the fuse controller and the other memories are serially connected to each other and to the memory which is connected to the fuse controller" as claimed in the independent claim 19.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

6. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Pho M. Luu whose telephone number is 571.272.1876. The examiner can normally be reached on M-F 8:00AM – 5:00PM.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's Supervisor, Richard Elms, can be reached on 571.272.1869. The official fax number for the organization where this application or proceeding is assigned is 703.872.9306 for all official communications.

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PML 19 June 2004